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ELEN 21L F 2:15pm

Prelab#7

1. Symbol Design
   1. Game Symbols
      1. 1100011-upper circle
      2. 1011100-lower circle
      3. 0110110-two lines vertical
      4. 0001001-two lines horizontal
   2. Status Symbols
      1. Win-1111111
      2. Lose-0111000
      3. Progress-1110001
2. Status code Verilog

module my\_status\_code(PB1, EQ, codeS)

input PB1, EQ;

ouput [3:0] codeS; //win=1101, lose=1110, progress=1111

always@(\*)

begin

case({PB1,EQ})

1: codeS=4’b1100;

2: codeS=4’b1101;

3: codeS=4’b1110;

4: codeS=4’b1111;

endcase

end

endmodule

1. A screenshot of a cell phone

   Description automatically generated

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